

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	(indicate or indicating) same (state adj2 signal)	▲
	same valid same invalid same l2	▼

Display:	<input type="text" value="10"/>	Documents in Display Format:	<input type="text" value="-"/>	Starting with Number	<input type="text" value="1"/>
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Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search History

DATE: Saturday, February 18, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
side by side			
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L31</u>	(indicate or indicating) same (state adj2 signal) same valid same invalid same l2	4	<u>L31</u>
<u>L30</u>	(indicate or indicating) same (state adj2 signal) same valid same invalid	63	<u>L30</u>
<u>L29</u>	l26 same l28	3	<u>L29</u>
<u>L28</u>	(enable or strobe) adj2 generator	5473	<u>L28</u>
<u>L27</u>	L26 and SDRAM	362	<u>L27</u>
<u>L26</u>	l2 same l1	1396	<u>L26</u>
<u>L25</u>	l19 and SDRAM	3	<u>L25</u>
<u>L24</u>	L23 and l19	0	<u>L24</u>
<u>L23</u>	l2 adj5 unstable	13	<u>L23</u>
<u>L22</u>	l2 same (timing adj2 loop) same unstable	1	<u>L22</u>
<u>L21</u>	L20 and l19	2	<u>L21</u>
<u>L20</u>	l2 same (unstable or glitch)	430	<u>L20</u>
<u>L19</u>	strobe same (state adj2 signal) same l2	83	<u>L19</u>
	l1 same (generate or generating) same command same l2 same (strobe or		

<u>L18</u>	enable)	19	<u>L18</u>
<u>L17</u>	strobe same l2 same (glitch or unstable)	16	<u>L17</u>
<u>L16</u>	l14 and (synchronous adj2 memory)	2	<u>L16</u>
<u>L15</u>	l14 and SDRAM	3	<u>L15</u>
<u>L14</u>	l2 adj6 ((timing adj2 loop) or glitch or unstable)	54	<u>L14</u>
<u>L13</u>	l2 same ((timing adj2 loop) or glitch or unstable)	455	<u>L13</u>
<u>L12</u>	(l2 adj5 (ouput)) same ((timing adj2 loop) or glitch or unstable)	0	<u>L12</u>
<u>L11</u>	(l2 adj5 (ouput)) same (validate or invalidate) same ((timing adj2 loop) or glitch or unstable)	0	<u>L11</u>
<u>L10</u>	l2 adj5 (glitch or unstable)	41	<u>L10</u>
<u>L9</u>	l2 same (glitch or unstable) same signal same valid same invalid	3	<u>L9</u>
<u>L8</u>	L6 same valid same invalid	6	<u>L8</u>
<u>L7</u>	L6 same valid or invlaid	208	<u>L7</u>
<u>L6</u>	l2 same l3	1986	<u>L6</u>
<u>L5</u>	L4 same l3 same l2 same l1	0	<u>L5</u>
<u>L4</u>	memory adj2 bank	15468	<u>L4</u>
<u>L3</u>	(enable adj2 signal)	86647	<u>L3</u>
<u>L2</u>	state adj2 (generator or machine)	55443	<u>L2</u>
<u>L1</u>	((bank or memory) adj2 controller)	78469	<u>L1</u>

END OF SEARCH HISTORY

Searches for User *kmclean2* (Count = 22708)

Queries 22659 through 22708.

Find

S #	Updt	Database	Query	Time	Comment
<u>S22708</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	((memory or SDRAM) same (enable or valid or invalid) same (unstable or error)) and (bank adj2 controller) and SDRAM	2006-02-18 20:07:02	
<u>S22707</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	((memory or SDRAM) or (state adj2 machine)) same (enable or valid or invalid) same (unstable or error)	2006-02-18 20:06:34	
<u>S22706</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	(enable adj2 signal same enable adj2 signal) and SDRAM and (bank adj2 controller)	2006-02-18 20:04:29	
<u>S22705</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	((memory or SDRAM) or (state adj2 machine)) same (enable adj2 signal)	2006-02-18 20:04:11	
<u>S22704</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	(enable adj2 signal and enable adj2 signal and bank adj2 controller) and SDRAM	2006-02-18 20:02:54	
<u>S22703</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	((memory or SDRAM) or (state adj2 machine)) and (enable adj2 signal) and (bank adj2 controller)	2006-02-18 20:02:44	
<u>S22702</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	(state adj2 generator) or (state adj2 machine)	2006-02-18 20:02:24	
<u>S22701</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	((memory or SDRAM) same SDRAM) and (bank adj2 controller)	2006-02-18 19:59:59	

		and (state adj2 generator)	
<u>S22700</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD ((memory or SDRAM) same SDRAM) and (bank adj2 controller)	2006-02-18 19:58:52
<u>S22699</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (enable adj2 signal) same SDRAM	2006-02-18 19:58:41
<u>S22698</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD ((memory or SDRAM) same (unstable)) and (enable adj2 signal)	2006-02-18 19:57:04
<u>S22697</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD ((memory or SDRAM) same (unstable adj2 condition)) same memory	2006-02-18 19:56:32
<u>S22696</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD ((memory or SDRAM) same (unstable)) and SDRAM	2006-02-18 19:55:28
<u>S22695</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (state adj2 generator) same (unstable)	2006-02-18 19:55:09
<u>S22694</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (state adj2 generator) same (unstable adj2 condition)	2006-02-18 19:54:55
<u>S22693</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD ((memory or SDRAM) same (memory or SDRAM)) and SDRAM	2006-02-18 19:51:56
<u>S22692</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (state adj2 generator) same (enable adj2 signal)	2006-02-18 19:51:46
<u>S22691</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD state adj2 generator	2006-02-18 19:51:34
<u>S22690</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (SDRAM and (state adj2 generator) same (state adj2 machine)) and (enable adj2 signal)	2006-02-18 19:48:39
<u>S22689</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD SDRAM and ((state adj2 generator) same (state adj2 machine))	2006-02-18 19:48:32
<u>S22688</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD (enable adj2 signal) same ((state adj2 generator) same (state adj2 machine))	2006-02-18 19:47:28
<u>S22687</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD enable adj2 signal	2006-02-18

			19:47:09
<u>S22686</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD((memory or SDRAM)) and (enable adj2 (generator or controller)) and ((state adj2 generator) same (state adj2 machine))	2006-02-18 19:44:34
<u>S22685</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD((memory or SDRAM)) and (enable adj2 (generator or controller)) and (bank adj2 controller)) and ((state adj2 generator) same (state adj2 machine))	2006-02-18 19:43:56
<u>S22684</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD(memory or SDRAM)	2006-02-18 19:43:40
<u>S22683</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBDenable adj2 (generator or controller)	2006-02-18 19:43:31
<u>S22682</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBDbank adj2 controller	2006-02-18 19:43:16
<u>S22681</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD(state adj2 generator) same (state adj2 machine)	2006-02-18 19:42:50
<u>S22680</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD(non adj volatile adj2 (memory or RAM)) same (controller or (control adj unit)) same ((host or cpu or processor)) same (chip adj2 select) same shared	2006-02-18 19:36:14
<u>S22679</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBD(non adj volatile adj2 (memory or RAM)) same (controller or (control adj unit)) same ((host or cpu or processor)) same (chip adj2 select)	2006-02-18 19:35:25
<u>S22678</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBDbit same (enable or enabling) same (disable or disabling) same (shared adj2 bus)	2006-02-18 19:31:45
<u>S22677</u>	<u>U</u>	PGPB,USPT,EPAB,JPAB,DWPI,TDBDbit same ((turn or turning) adj2 on) same ((turn or turning) adj2	2006-02-18 19:31:10